

Bluetooth® Smart typeZF/ZY Development Environment Overview

Embedded System Development Sec.
Module Business Unit



TypeZF/ZY Design Kit contents



Hardware contents

- Mother board
- Type ZF or ZY daughter board
- J-Link debugging adapter

Software contents

- Software on download-link
- Keil™ tools, with DA14580 peripherals
- Sample application/Profile source code
- OTP programming utility
- Power consumption profiling tool
- Documentation

Segger J-link Lite for ARM



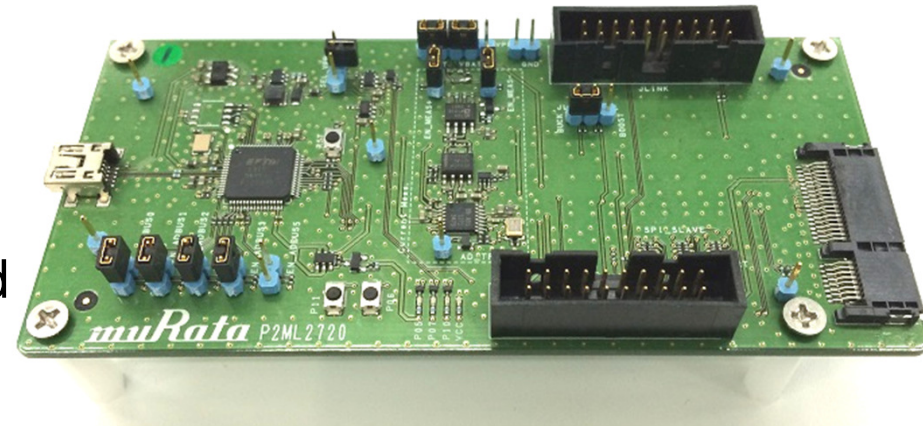
Design Kit Motherboard

Daughterboard with
TypeZF or ZY mounted

Design Kit Motherboard



- JTAG interface for programming and debugging
- Interface to the TypeZF/ZY Daughter board
- OTP supply voltage generator (6V8)
- Mother board is powered through the USB interface.
- 2 LEDs and 2 pushbuttons for application development
- USB to serial port converter (UART for debugging and/or external host application interface. SPI for current consumption evaluation using SmartSnippets).
- Current evaluation A/D conversion circuitry
- All I/Os are exposed onto 2.54mm pin headers for easy prototyping



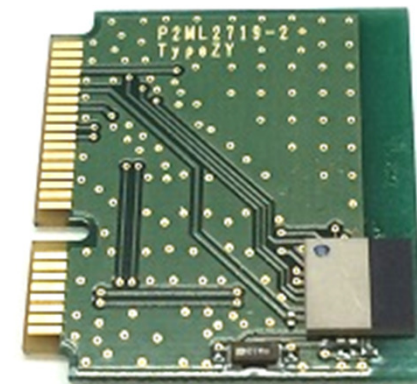
Design Kit Daughterboard



- Stand-alone implementation of TypeZF or ZY (except for power source)
- Two variants available: TypeZF and TypeZY
- All I/Os are brought to the card edge connectors (interface to motherboard)



TypeZF variant



TypeZY variant