
PIC16F688 Rev. A4 Silicon/Data Sheet Errata

The PIC16F688 (Rev. A4) parts you have received conform functionally to the Device Data Sheet (DS41203D), except for the anomalies described below.

Microchip intends to address all issues listed here in future revisions of the **PIC16F688 silicon**.

1. Module: EUSART – Writing to TXREG Overwrites Values (Synchronous Mode Only)

Writing to the TXREG faster than the baud rate in Synchronous mode will overwrite the previous value instead of double-buffering as in Asynchronous mode.

Work around

Load the first character into TXREG and then wait for a TX interrupt or check the TXIF bit before writing each additional character to the TXREG.

2. Module: EUSART – Transmitter not Reset When Enabled

The EUSART module does not perform a Reset when TXEN is set. This allows data to be written to TXREG before the transmitter is configured.

Work around

None.

Silicon Revision History

Improvements from Rev. A3 to Rev. A4 Silicon are as follows:

- The POR circuit has been enhanced by raising the rearm voltage to 1.2V.
- The EMI/ESD/EFT performance has been enhanced to improve the devices performance in harsh applications such as: line-powered devices, furnace igniters and automotive systems.

Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (41203D), the following clarifications and corrections should be noted.

N/A.

PIC16F688

REVISION HISTORY

Rev A Document (10/2008)
Original errata document.

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
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