

## Super Low Distortion Multilayer Ceramic Capacitors

## JMK105SD272KP-F



## ■ Features

- Item Summary  
2700pF±10%, 6.3V,  
LowDistortionStandard, 0402/1005 (EIA/JIS)
- Lifecycle Stage  
Mass Production
- Standard packaging quantity (minimum)  
Taping paper 15000pcs

## ■ Products characteristics table

Capacitance	2700 pF ± 10 %
Case Size (EIA/JIS)	0402/1005
Rated Voltage	6.3 V
tan δ (max)	0.1 %
Operating Temp. Range (EIA)	-55 to +125 °C
Operating Temp. Range (JIS)	-55 to +125 °C
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	10 GΩ
RoHS2 Compliance (10 subst.)	Yes
REACH Compliance (181 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow

## ■ External Dimensions

Dimension L	1.0 ±0.05 mm
Dimension W	0.5 ±0.05 mm
Dimension T	0.3 ±0.03 mm
Dimension e	0.25 ±0.10 mm

2018.08.22

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.  
 TAIYO YUDEN reserves the right to make change to the data at any time without notice.  
 Before making final selection, please check product specification.

# Multi Layer Ceramic Capacitor **TAIYO YUDEN**

-Electrical Characteristics Data-

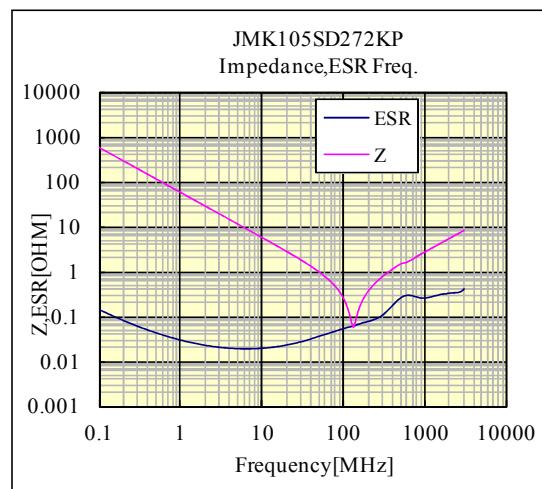
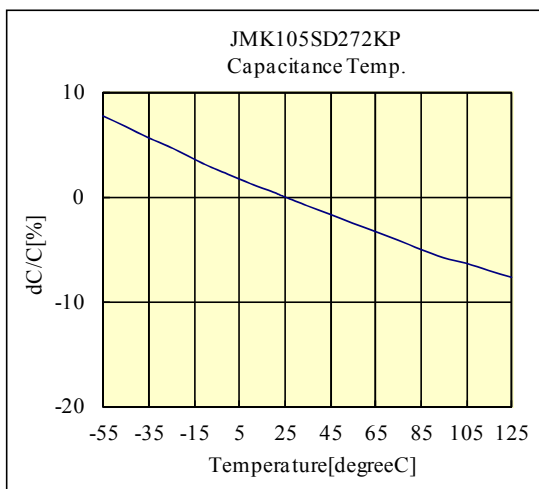
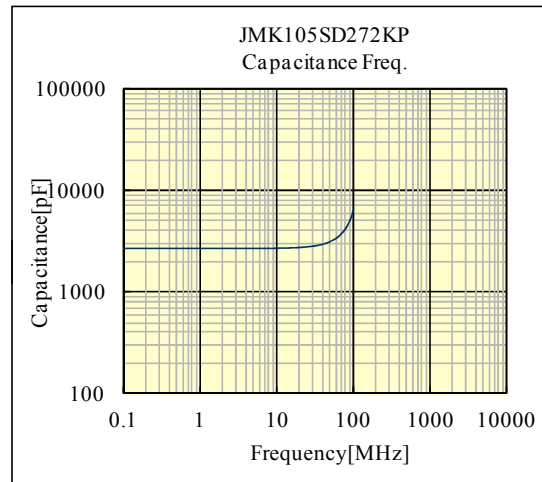
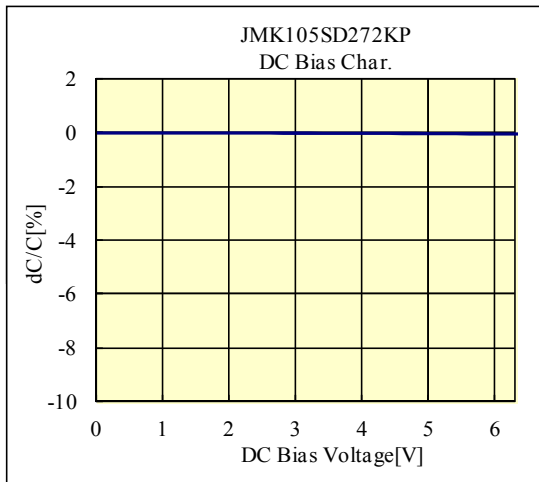
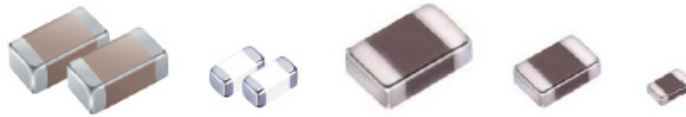
16CJA02

RoHS compliance product

## ○JMK105SD272KP

**6.3V 1005 2700pF +/- 10% t=0.33mmMAX**

**6.3V 0402 2700pF +/- 10% t=0.013inchMAX**



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